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**\*\* CONTINUING DATA \*\*\*\*\***

This application is a 371 of PCT/JP04/00869 01/29/2004

**\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 2003-030642 02/07/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>Anilay K. Moudgalya AA</i> Initials Examiner's Signature				

**ADDRESS**

23353

**TITLE**

Silicon semiconductor substrate and its manufacturing method

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